

PATENT

DOCKET NO. 30585/7

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): David L. Reese, et al.

Serial No.: 09/330,852

Art Unit: 2763

Filed: June 11, 1999

Examiner: unassigned

Title: PROFILING RANGES OF EXECUTION OF A COMPUTER PROGRAM

COMMISSIONER FOR PATENTS

Washington D.C. 20231

**PRELIMINARY AMENDMENT AND TRANSMITTAL OF FORMAL DRAWINGS**

Kindly amend the application as follows.



RECEIVED  
NOV -7 2000  
TIC 2700 MAIL ROOM

**In the Specification:**

Please amend the specification as follows.

At page 5, line 2, change "schedules" to -- scheduled --.

At page 5, line 5, change "a one" to --one--.

At page 8, line 2, change "which at which at" to --which at--.

At page 9, line 28-29, change "for translating address references are" to --translates address referenced--.

At page 10, line 4, change "Preferred embodiments" to --Embodiments--.

At page 11, line 23, change "Preferred embodiments" to --Embodiments--.

At page 12, line 16, add a period at the end of the line.

At page 14, line 1, change "Preferred embodiments" to --Embodiments--.

At page 20, lines 9-10, change "the I-cache" to --I-cache 112--.

At page 25, lines 7-12, replace the sentence "Translator 124 ... data)." with the following: --Translator 124 also assumes that all memory references are to well-behaved memory. ("Well-behaved memory" is a memory from which a load will receive the data last stored at the memory location. Non-well -behaved memory is typified by memory-mapped device controllers, also called "I/O space," where a read causes the memory to change state, or

I certify that this correspondence, along with any documents referred to therein, is being deposited with the United States Postal Service on November 1, 2000 as First Class Mail in an envelope with sufficient postage addressed to The Commissioner for Patents, Washington D.C. 20231.

*David L. Reese*